

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Varadarajan Srinivasan et al

SERIAL NO.: not assigned

FILING DATE: 2/9/2004

Title: *Content Addressable Memory Having Column Redundancy*

Examiner: unknown

Art Unit: 2824

**INFORMATION DISCLOSURE**  
**STATEMENT**

Commissioner for Patents  
Alexandria, VA 22313

Sir:

Pursuant to 37 CFR 1.56, 1.97, and 1.98(d), Applicants bring the following document(s) to the Examiner's attention in the above-captioned application.

1. U.S. Patent Application Serial No. 10/143,051 entitled Content Addressable Memory Having Column Redundancy and filed on 5/10/2002, which is the parent of the present application.

A PTO Form 1449 listing this document is enclosed. Pursuant to 37 CFR 1.98(d), a copy of this application is not provided because Applicant claims benefit of the prior filed parent application under 35 USC §120.

In addition, citation of the above document shall not be construed as:

1. an admission that the document is necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in 1.56(b).

Respectfully submitted,



Dated: February 9, 2004

William L Paradise III  
Reg. No. 38,990

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313 on February 9, 2004.



By: \_\_\_\_\_  
William L Paradise III

<b>INFORMATION DISCLOSURE CITATION</b>  PTO-1449	ATTY. DOCKET NO. P208-C1/WLP	SERIAL NO. not assigned
	APPLICANT Srinivasan et al	
	FILING DATE 2/9/2004	GROUP 2824

**U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

**FOREIGN PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

	U.S. Patent Application Serial No. 10/143,051 entitled Content Addressable Memory Having Column Redundancy and filed on 5/10/2002.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.